2018 VI 11	1430	Seat No. :
Time : 2 Hours	DIGITAL E	LECTRONICS AND COMPUTERS
	Subject Code	
Total No. of Questions : 5	(Printed Pages : 3	Maximum Marks : 50
iii) AII q iv) Figu	·	ion and sub-question clearly. sory. e full marks.
1. A) Fill in the blanks.		[2]
i) The decimal equiva	alent of (F) ₁₆ is	
ii) When input signals output signal will be		pplied to an EX-NOR gate, its
B) Answer the following:		
i) With a neat circuit o	diagram, explain 4-bit ı	ring counter. [3]
ii) What are bubbled g explain bubbled OR	•	f logic symbol and truth table, [3]
C) Answer the following:		[2]
What is shift register? I	Draw a neat circuit dia	gram of 4-bit shift right register.
2. A) Define the following:		[2]
i) Full Adder		
ii) Buffer Register.		



	B)	Answer the following:	
		i) Draw a neat circuit diagram of JK flip flop and explain its 'Reset' mode.	[3]
		ii) Explain the terms 'volatile memory' and 'non-volatile memory'. Give one example of each.	[3]
	C)	Answer the following:	
		Draw the block diagram of counter type analog to digital converter.	[2]
3.	A)	Fill in the blanks:	[2]
		i) The 1's complement of binary number (1010) ₂ is	
		ii) The 8080A microprocessor consists of number of functional chips.	
	B)	Do as directed :	[3]
		i) Convert (4429) ₁₀ to its octal equivalent.	
		ii) Convert (101100010) ₂ to its decimal equivalent	
		iii) Convert (1993) ₁₀ to its hexadecimal equivalent.	
	C)	Answer the following in detail :	
		Draw a diagram of typical microprocessor and state the functions of Arithmetic Logic Unit (ALU).	[5]
		OR	
		Draw a block diagram of computer system state any three functions of Central Processing Unit (CPU).	[5]
4.	A)	Answer the following in one sentence.	[2]
		i) What is the clock frequency of 8085 microprocessor?	
		ii) Write the formula to obtain total time period of the output waveform of a symmetrical astable multivibrator.	

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	B)	Answer the following:	
		State Demorgan's second theorem. Show that $\overline{A.B} = \overline{A} + \overline{B}$.	[3]
	C)	Answer the following in detail :	
		Explain 'Mouse' as an input device and 'Plotter' as an output device in the computer system.	[5]
		OR	
		Explain 'Floppy disk' as storage device, state the diameters of mini-floppy and micro-floppy.	[5]
5.	An	nswer the following:	
	i)	Draw neat logic diagrams converting a NOR gate to AND gate and OR gate.	[2
	ii)	State any two uses of 'Schmitt Trigger'.	[2
	iii)	Differentiate between 'edge triggering' and 'level clocking' with two points of difference in flip-flops.	[2]
	iv)	With the help of logic symbol, explain 'multiplexer'.	[2
	v)	State any two advantages of binary ladder.	[2

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