Paper Code – 5602-6602

M.Sc. Semester-III (Electronics)

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MSELE-302 CC : MICROPROCESSOR PERIPHERALS AND ADVANCE MICROPROCESSORS

કુલ સમય : ૬૦ મીનીટ **કુલ પ્રશ્નો : ૫૦** **વિદ્યાર્થીએ ટીક કરવાના પ્રશ્નો : ૩૫**

નોંધ : વિદ્યાર્થીએ માત્ર ૩૫ પ્રશ્નો જ ટીક કરવાના રહેશે. જો વધારે ટીક કરેલ હશે તો પ્રથમ ૩૫ પ્રશ્નો જ ગણતરીમાં લેવામાં આવશે.

1. Transceiver is _____.
   (A) Transmitter only (B) Receiver only (C) Transmitter and Receiver both (D) None of these options
2. Which IC is popular Transceiver?
   (A) 74245 (B) 7400 (C) 7402 (D) None of these options
3. Which pin of IC 74245 Transceiver decides direction.
   (A) DIR (B) A0 (C) B0 (D) None of these options
4. Which one is not input device?
   (A) Keyboard (B) Scanner (C) Display (D) microphone
5. Which one is Handshake signal?
   (A) D0 (B) A0 (C) STB (D) None of these options
6. Which one is multipurpose programmable device?
   (A) 8155 (B) 8085 (C) 8086 (D) 8279
7. IO/M' signal is used to differentiate _____.
   (A) Input- Output and Memory (B) Insert other in memory (C) Input and signal (D) Output and Display
8. RESET pin of 8155 is normally connected with which pin of 8085?
   (A) IO/M' (B) A8 (C) RESET OUT (D) HOLD
9. 8155 is _____ pin DIP.
   (A) 20 (B) 30 (C) 10 (D) 40
10. 8155 includes _____ bytes of memory.
    (A) 256 (B) 2000 (C) 3000 (D) 64K
11. 8155 Time can work in _____ different modes.
    (A) six (B) Twelve (C) Three (D) Four
12. In control word of 8155 if, D7=D6=0 means, the timer will _____.
    (A) start (B) stop (C) No operation (D) None of these options
13. The meaning of BF handshake signal is _____.
    (A) Buffer full (B) Bed Fault (C) Both Functions (D) Boy friend
14. If the address of CWR in 8155 is 20H then what will be the address of PORT A?
    (A) 19H (B) 21H (C) 22H (D) 24H
15. If the address of CWR in 8155 is 00H then what will be the address of PORT C?
    (A) 01H (B) 02H (C) 03H (D) 04H
16. Which IC is programmable Keyboard Display Interface?
    (A) 74245 (B) 8279 (C) 8155 (D) 8255
17. RL0 to RL7 are input to which section of 8279?
    (A) KB section (B) Display section (C) CPU interface section (D) None of these options
18. DB0 to DB7 pins of 8279 needs to connect with which bus of 8085?
    (A) Address bus (B) Control bus (C) Data bus (D) None of these options
19. 16 x 8 Display RAM is present in which IC?
    (A) 8155 (B) 8255 (C) 8279 (D) 8085
20. KB debounce and control block is used for _____.
    (A) To control multiple key pressing in microseconds (B) To read KB status
    (C) To display Key pressed (D) None of these options
21. The FIFO to read KB RAM. Here FIFO means_____.
    (A) Fast input Fast output (B) First In First Out (C) Few input and few output (D) None of these options
22. The 8255 is _____.
    (A) 8-bit microprocessor (B) 8-bit microcontroller (C) KB Display Interface (D) Programmable Peripheral Interface
23. What is BSR Mode in 8255?
    (A) Byte Setting Register (B) Byte set/Reset (C) Bit Set/Reset (D) Bubble strong Random
24. If address of Port A of 8255 is 20H, what will be the address of Port C?
    (A) 20H (B) 22H (C) 24H (D) 26H
25. Which bit of 8255 Control word will decide BSR or I/O mode?
    (A) D0 (B) D4 (C) D7 (D) D1
26. If 8255 Control word is 0FH means _____.
    (A) Set PC0 (B) Reset PC0 (C) Set PC7 (D) Reset PC7
27. The meaning of CS pin of 8255 _____.
    (A) Clear set (B) Chip Select (C) Come and see (D) None of these options
28. The 8255 can work in Mode ____.
    (A) Two (B) Three (C) Four (D) Five

29. The programmable Timer Interval is_____.
(A) 8255 (B) 8254 (C) 8279 (D) 8155
30. The counter 1, Counter 2 and Counter 3 are in the block diagram of _____ I(C)
(A) 8255 (B) 8254 (C) 8279 (D) 8155
31. SC1 and SC0 in 8254 means _____.
(A) Select counter (B) Settle down (C) See clear (D) None of these options
32. BCD means _____in codes.
(A) Binary counter Display (B) Byte come and Detected (C) Binary coded Decimal (D) None of these options
33. Programmable interval timer 8254 can work in ____ different modes.
(A) 3 (B) 4 (C) 5 (D) 6
34. The IC 8259A is known as_____.
(A) Transciver B Programmable peripheral interface (C) Timer IC (D) Programmable interrupt controller
35. With help of one 8259A the INTR can be extended upto ____ interrupts.
(A) 2 (B) 8 (C) 16 (D) 24
36. 3 to 8 decoder has ___ inputs and ____ outputs.
(A) 1, 5 (B) 3, 8 (C) 8, 3 (D) 8, 8
37. The IC 8237 is _____.
(A) PPI (B) Timer (C) DMA controller (D) None of these options
38. 3-bit D/A converter has how many outputs?
(A) 0 (B) 1 (C) 2 (D) 3
39. The microprocessor can read _____ signal.
(A) Digital (B) Analog (C) Both Analog and Digital (D) None of the given options
40. Successive approximation is a type of _____.
(A) DAC (B) ADC (C) PPI (D) Timer
41. 8086 can access up to?
(A) 512KB (B) 1Mb (C) 2Mb (D) 256KB
42. 8086 has ___ address bus.
(A) 16-bit (B) 18-bit (C) 20-bit (D) 24-bit
43. Which flag is set to 1 when the result of arithmetic or logical operation is zero else it is set to 0?
(A) Binary bit (B) Zero flag (C) Sign flag (D) Overflow flag
44. Which flag represents the result when the system capacity is exceeded?
(A) Carry flag (B) Auxiliary flag (C) Trap flag (D) Overflow flag
45. It is an edge triggered input, which causes an interrupt request to the microprocessor.
(A) NMI (B) D0 (C) INTA (D) ALE
46. The different ways in which a source operand is denoted in an instruction is known as
(A) Instruction Set (B) Interrupts (C) 8086 Configuration (D) Addressing Modes
47. A _____ is a specially designed circuit on microprocessor chip which can perform the same task very quickly, which the microprocessor performs
(A) Coprocessor configuration (B) Closely coupled configuration
(C) Loosely coupled configuration (D) None of these options
48. Which of the following is not a features of 8086?
(A) It uses two stages of pipelining (B) It is available in 3 versions based on the frequency of operation
(C) Fetch stage can prefetch up to 6 bytes of instructions (D) It has 1512 vectored interrupts.
49. Which is not the control bus signal ?
(A) READ (B) WRITE (C) RESET (D) None of these options
50. The DIP pin configuration of 8086 is available in the_____
(A) 40 pin (B) 50 pin (C) 30 pin (D) 20 pin